IE UNITED STATES PATENT AND TRADEMARK OFFICE

Approcant:

Feng Pan

Title:

High Voltage Ripple Reduction and Substrate Protection

Application No.:

10/811,074

Filing Date:

March 26, 2004

Examiner:

Unassigned

Group Art Unit:

2838

Docket No.:

SNDK.307US1

Conf. No.:

8070

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicant calls the documents listed on the enclosed Form PTO-1449 to the Examiner's attention in this patent application.

Citation of these documents shall not be construed as (1) an admission that the documents are prior art with respect to the invention or inventions claimed in this application, (2) a representation that a search has been made (other than as indicated by any cited document), or (3) an admission that the cited information is, or is considered to be, material to patentability as defined in § 1.56(b).

This application has a filing date after June 30, 2003. Copies of the U.S. Patent(s) and U.S. Published Patent Application(s) documents listed on the accompanying Form PTO-1449 are not enclosed.

This information disclosure statement is submitted under 37 C.F.R. § 1.97(b) and consequently no fee should be required. The Commissioner is authorized, however, to charge

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any fee that may be required, or to credit any overpayment, against Deposit Account No. 502664. This form is being submitted in duplicate.

Certificate of Mailing Under 37 CFR 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope address to:

Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on August 10, 2004.

Franklin Dyer

Respectfully submitted,

Gerald P. Parsons

Reg. No. 24,486

Attorney Docket No.: SNDK.307US1

Application No.: 10/811,074

Sheet 1 of 1

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